



IES MASTER

Institute for Engineers (IES/GATE/PSUs)

ESE-2021 Conventional Test Schedule, Electronics & Telecomm. Eng.

Date	Topic
10th Oct. 2021	MAT-2, BEE-2, NT-2
	BEX-2, MI-3, CS-3, AET-2
17th Oct. 2021	CO-2, ADC-4, EMT-2
	MAT-2, BEE-2, NT-2, AET-1, BEX-3
24th Oct. 2021	ADC-2, ACT-2, EMT-3
	CO-2, ADC-4, BEX-2, MI-3, CS-3
31st Oct. 2021	NT-3, COMM-3
	BEX-1, BEX-3, CS-2, EMT-3, ACT-2, ADC-2
5th Nov. 2021	Full Length-1 (Test Paper-1 + Test Paper-2)
9th Nov. 2021	Full Length-2 (Test Paper-1 + Test Paper-2)
14th Nov. 2021	Full Length-3 (Test Paper-1 + Test Paper-2)

Test Type

Timing

Day

Conventional Test	_____	10:00 A.M. to 1:00 P.M.	_____	Sunday
Conventional Full Length Test Paper-1	_____	10:00 A.M. to 1:00 P.M.	_____	Tuesday
Conventional Full Length Test Paper-2	_____	02:00 P.M. to 5:00 P.M.	_____	Tuesday

Note : The timing of the test may change on certain dates. Prior information will be given in this regard.

*N.T. : New Topic. *R.T. : Revision Topic

Call us : 8010009955, 011-41013406 or Mail us : info@iesmaster.org

Subject Code Details

Basic Electronics Engineering (BEX)	BEX-1	BEX-2		BEX-3
	<ul style="list-style-type: none"> ◆ Basics of Semiconductors ◆ Diode : Basics, Characteristics & its types ◆ BJT, JFET, MOSFET-Basic Structure & Characteristics 	<ul style="list-style-type: none"> ◆ Transistor Amplifiers ◆ Oscillators & Other circuits ◆ Basic of Linear ICs ◆ Operational Amplifier & their applications 		<ul style="list-style-type: none"> ◆ Basics of ICs; Bipolar, MOS & CMOS ICs ◆ Optical Sources / Detectors ◆ Basics of Optoelectronics & Applications
Basic Electrical Engineering (BEE)	BEE-1		BEE-2	
	<ul style="list-style-type: none"> ◆ Single Phase AC circuits ◆ Transformer ◆ DC Machine 		<ul style="list-style-type: none"> ◆ Induction Machine ◆ Synchronous Machine ◆ Electrical Power Sources, Basics of Batteries & its uses 	
Material Science (MAT)	MAT-1		MAT-2	
	<ul style="list-style-type: none"> ◆ Crystalline Structure ◆ Dielectric properties of matter ◆ Ceramic materials ◆ Magnetic properties of materials 		<ul style="list-style-type: none"> ◆ Insulating laminates for electronics ◆ Conductors & Superconductors ◆ Semiconductor & Optical materials ◆ Nano materials Nano-optical / Magnetic / Electronic materials 	
Electronic Measurement and Instrumentation (MI)	MI-1	MI-2		MI-3
	<ul style="list-style-type: none"> ◆ Error analysis & basics of measurement ◆ Basic measuring instruments 	<ul style="list-style-type: none"> ◆ Measurement of Resistance ◆ AC Bridges ◆ Potentiometer ◆ Cathode Ray Oscilloscope (CRO) ◆ Q-meter 		<ul style="list-style-type: none"> ◆ Basics of electronic measurements ◆ Digital & electronic voltmeter ◆ Digital frequency meter ◆ Transducers & Displays ◆ Basics of Telemetry ◆ Data Acquisition System
Network Theory (NT)	NT-1		NT-2	
	<ul style="list-style-type: none"> ◆ Network elements ◆ Network theorems ◆ 2-port networks 		<ul style="list-style-type: none"> ◆ Transient and Steady State Response ◆ Steady State Sinusoidal analysis ◆ Resonance 	
Analog and Digital Circuits (ADC)	ADC-1	ADC-2	ADC-3	ADC-4
	<ul style="list-style-type: none"> ◆ Small Signal equivalent of Diodes, BJTs and FETs ◆ Different Diode Circuits ◆ Biasing and Stability of BJTs & JFET amplifier circuits 	<ul style="list-style-type: none"> ◆ Analysis / Design of amplifiers signal & multi-stage ◆ Feedback & its uses ◆ Active filters, timers, multipliers, wave shaping 	<ul style="list-style-type: none"> ◆ Boolean Algebra & Logic Gates ◆ Combinational circuits : Design & Applications 	<ul style="list-style-type: none"> ◆ Sequential circuits: Design & Applications ◆ Design IC Logic families ◆ A/D & D/A converters
Among and Digital Communication (COMM)	COMM-1		COMM-2	
	<ul style="list-style-type: none"> ◆ Analog Communication Systems 		<ul style="list-style-type: none"> ◆ Digital Communication Systems 	
Control Systems (CS)	CS-1		CS-2	
	<ul style="list-style-type: none"> ◆ Signals and Systems ◆ System Realization ◆ Transforms & their Applications 		<ul style="list-style-type: none"> ◆ Basics of Control Systems ◆ Block Diagram & Signal Flow Graphs ◆ Time Response Analysis ◆ Routh Hurwitz criteria & Root Locus Technique 	
Computer Organization and Architecture (CO)	CO-1		CO-2	
	<ul style="list-style-type: none"> ◆ Basics of Computer Organization ◆ Operating Systems 		<ul style="list-style-type: none"> ◆ Database Management Systems ◆ Data Structure and Programming 	
Electromagnetics (EMT)	EMT-1		EMT-2	
	<ul style="list-style-type: none"> ◆ Elements of Vector Calculus ◆ Electrostatics ◆ Magnetostatics 		<ul style="list-style-type: none"> ◆ Maxwell's Equations ◆ Electromagnetic Wave propagation through different media ◆ Transmission Lines 	
Advanced Electronics Topics (AET)	AET-1		AET-2	
	<ul style="list-style-type: none"> ◆ VLSI Technology ◆ VLSI Design ◆ Mealy and Moore circuit design ◆ Pipeline concept and functions ◆ Designs for testability and examples 		<ul style="list-style-type: none"> ◆ Digital Signals Processing ◆ Digital Filters ◆ Speech / Audio / Radar Signal Processing ◆ Microprocessors and Microcontrollers ◆ Embedded Systems 	
Advanced communication Topics (ACT)	ACT-1		ACT-2	
	<ul style="list-style-type: none"> ◆ Communication Networks : Principles / Practices / Technologies / Uses / OSI Model / Security ◆ Basic packet multiplexed streams / scheduling ◆ Protocols (TCP / TCP-IP) ◆ Cellular Networks : Types, Analysis 		<ul style="list-style-type: none"> ◆ Microwave & Satellite Communication ◆ Fiber Optic Communication 	